### KLCS-47E, 47, 470, 470A Series

#### Features

	47E Series	47 Series	470 Series	470A Series			
Maximum ROM size	4K	× 8	8K × 8				
Maximum RAM size	256 × 4	768 × 4	1024	1024 × 4			
Minimum instruction execution time	$\begin{array}{l} \text{1.0} \text{ \mus (at 8Mbz, } V_{DD} = 2.7 V \sim 5.5 V) \\ \text{1.3} \text{ \mus (at 8Mbz, } V_{DD} = 4.5 V \sim 5.5 V) \\ \text{1.9} \text{ \mus (at 8Mbz, } V_{DD} = 2.7 V \sim 5.5 V) \\ \text{3.2} \text{ \mus (at 8Mbz, } V_{DD} = 2.7 V \sim 5.5 V) \\ \end{array}$	1.9µs (at 4.2Mz, V <sub>DD</sub> =4.5V~6V)	$1.3 \mu s$ (at 6MHz, $V_{DD}$ =4.5V $\sim$ 5.5V) $244 \mu s$ (at 32.8MHz, $V_{DD}$ =2.7V $\sim$ 5.5V)				
Number of instructions	9	0	92	105			
Number of interrupts	5/6		6				
Packages	DIP16~20 SOP16~28 SDIP28~42 SSOP30 QFP44	SDIP30~42 QFP44~80	SDIP28~64 QFP44~100	SDIP42~64 QFP44~80			

#### Basic functions

- Instructions: maximum 105, minimum instruction execution time: 1.0μs
- ROM table look-up instruction
- 5-bit to 8-bit data conversion instruction
- Subroutine nesting: maximum 15 levels
- Interrupt sources: 2 external, 4 internal
- Interval timer
- Serial interface

#### Additional functions

- VFT driver
- LCD driver
- LED driver
- Hold function (low power consumption mode)
- Multi-pin input/output
- D/A conversion (PWM) output
- A/D conversion input
- A/D converter input
- E<sup>2</sup>PROM
- 16-bit high-speed event counter
- On-screen display circuit
- DTMF generator
- DTMF receiver
- Watchdog timer
- Pulse generator
- Remote control pulse detector
- High-speed timer/counter
- Dual clock system
- One-time PROM

K	LCS	6-47E, 47 Serie	es	S	iel	ec	<u>:ti</u>	or	1	<u>Gι</u>	Jic	<u>et</u>													
			D	riv	er	SI	0	A/	A/D	Puls	e ou	tput	Rem	Wa	Hig	DTMF	OS	SD	Dual	Hold	Pa	ackag	ţе	0TP	Minin
ROM (byte)	RAM (nibbles)	Product No	LED	LCD	VFT	4-bit	8-bit	D converter	conversion input	PWM	PPG	Pulse	Remote control pulse detector	Watchdog timer	High-speed event counter	MF generator	Bar display	Character	al clock	ld function	SDIP(DIP)	QFP(SOP)	QFP(SSOP)	P type	( $ullet$ : 2.7V, $ullet$ : 2.2V) Minimum operating voltage
		KMP47C101P/M	4																	•	16	16		•	•
		KMP47C102P/M	4											•						•		20		•	•
1k	64	KMP47C103N/M	8				1							•						•	28	28		•	•
		† KMP47E187M												•						•		16		•	•
		† KMP47E186M												•						•		16		•	•
		KMP47C201P/M	4																	•	16	16		•	•
		KMP47C202P/M	4											•						•	20	20		•	•
		KMP47C203N/M	8				1							•						•	28	28		•	•
		KMP47C200BN/BF	8			1														•	42	44		•	•
		* KMP47C206P/M	5									1		•						•	20	20		•	
	128	KMP47C210AN/AF			20	1														•	42	44		•	
2k	120	KMP47C212AN			20	1														•	42				
		KMP47C231AN	8				1		5				•							•	30				
		KMP47C233AN	8						1											•	42				
		KMP47C241N/M	5			1		4						•						•	28	28		•	•
		KMP47C242BN	8					4				1		•						•	30			•	•
		KMP47C243N/M/DM	8				1	8				1		•						•	28	28		•	•
	192	KMP47C221ADF		24		1	1	-				1								•	40	4.4		•	
		KMP47C222N/F KMP47C400BN/BF	8	20		1	1	4				1		•					•	-		44 44	$\Box$	-	•
		KMP47C410AN/AF	0		20	1														_		44	$\square$	•	•
		KMP47C410AN/AF	$\vdash$		20	1		_												-	42	44	$\Box$	•	
		KMP47C412AN	-	24	20	1														_		64		•	
		KMP47C421ADI	$\vdash$	20		7	1	4				1		•					_	_		44		•	•
		KMP47C423ADF	$\vdash$	24		1	T	-#				1			1				_	_		64		_	
	256	KMP47C433AN	8			r			1						7					•	42	04			
	200	KMP47C440BN/BF	8	-		1				8				•							42	44	$\exists$	•	
		KMP47C441AN/AF	Ť		16	1		4		Ť				•								44		•	-
4k		KMP47C443N/M/DM	8				1	8				1		•						_		28		•	•
***		KMP47C446ADF	1	24		1	_	4				_		•					•	_		64	$\exists$	•	
		* † KMP47E486M	$\vdash$											•						•		28		•	•
	* † KMP47E487M												•						•		28		•	•	
		KMP47C407AN/AF	T			1						1				•				•	42	44		•	•
		KMP47C451BN										1				•					30			•	•
	<b>5</b> 00	KMP47C452BN/BF	$\top$			1						1				•				•		44		•	•
	768	KMP47C453AN/AF				1						1				•				•	42	44		•	•
		KMP47C454AN										1				•				•	30			•	•
	KMP47C456ADF		32		1						1		•		•			•			80			•	

<sup>\*:</sup> Under development †: USP4, 382, 279 owned by BULL CPU8

#### KLCS-47E Series

Product No			ROM (bytes)	RAM (nibbles)	I/O port	Minimum instruction execution time (µs)	Power Supply Voltage (V)	Package	Built-in One-time PROM product	
KMP47C101P/M			1k	64	11			DIP16	KMP47P201VP	
KMP47C201P/M			2k	128	11	1.3	2.2 to 5.5	SOP16	1000 471 201 71	
KMP47C102P/M	LED driver		1k	64		1.3	(note2)	DIP20	YZIN ATO ATTOROOTI TO ATTINA	
KMP47C202P/M	LED driver			15			SOP20	KMP47P202VP/VM		
KMP47C206P/M		2k	128	10	1.0	4.0~5.7	DIP20 SOP20	TMP47P206VP/VM		
KMP47C103N/M	0 14 010		1k	64	23		2.2 to 5.5	do todo	123 ATO 45710 AO 23 TO 3 AT TO 4	
KMP47C203N/M	8-bit SIO				23	1.3	(note2)	SDIP28 SOP28	KMP47P403VN/VM	
KMP47C241N/M	A/D converter		2k 12	128	21		2.7 to 6.0	5OI 20	KMP47P241VN/VM	
KMP47C243N/M/DM	4.40				23	1.0	2,2 to 5,5	SDIP28 SOP28	KMP47P443VN/VM/VDM	
KMP47C443N/M/DM	A/D converter 8-bit SIO		4k	256	20	1.0	(note 2)	SSOP30	ISSUE THE THOP IN THE POINT	
KMP47C222N/F	pulse output	T C'D duine	2k	192	22	1.0	004. 55	SDIP42	TZD 400 4500 400 TD X 6770	
KMP47C422N/F		LCD driver	4k	256	44	(244)	2.2 to 5.5	QFP44	KMP47P422VN/VF	
* † KMP47E186M	E <sup>2</sup> PROM (16byte), SPI		1k	64	11	1.3	2.0 to 5.5 (note 3)	SOP16	KMP47P186M(note 4)	
KMP47E187M	E <sup>2</sup> PROM (16byte), SPI		1k	64	11	1.3	2.0 to 5.5 (note 3)	SOP16	KMP47P187M	
* † KMP47E486M	E <sup>2</sup> PROM		4k	256	21	1.3	2.7 to 5.5	SOP28	KMP47W486M	
* † KMP47E487M	E <sup>2</sup> PROM		4k	256	21	1.3	2.7 to 5.5	SOP28	KMP47W487M	

\*: Under development †: USP4, 382, 279 owned by BULL CPU8

Note1: ( ); the minimum instruction execution time when low-frequency clock is used. Note2: OTP built-in Type is under consideration for high-temperature range/high-quality

applications. Note3 : 2.7 to 5.5V when oscillator is connected, 2.0 to 3.4V during CR oscillation. Note4: KMP47P186M (CR oscillation), KMP47P187M (oscillator version)

#### ■ KLCS-47 Series

Product No	Func	ROM (bytes)	RAM (nibbles)	I/O port	Minimum instruction execution time (µs)	Power Supply Voltage (V)	Package	Built-in One-time PROM product	
KMP47C200BN/BF KMP47C400BN/BF	Standard (LED o	lriver)	2k 4k	128 256	36		2.7 to 6.0	SDIP42 QFP44	KMP47P400VN/VF
KMP47C210AN/AF			2k	128				SDIP42	
KMP47C410AN/AF	1		4k	256	36	1.9		QFP44	KMP47P410AN/AF
KMP47C212AN	VFT driver		2k	128	or			CONTO 10	
KMP47C412AN	1	4k	256	35			SDIP42	-	
KMP47C221ADF	rop 1:		2k	192					Y21 470 470 401 A Y212
KMP47C421ADF	LCD driver				28	1.9		QFP64	KMP47P42LADF
KMP47C423ADF		High-speed event counter	4k	256	20	1.0	4.5 to 6.0	QLI OH	
KMP47C231AN	D/A conversion (PWM) output	4-bit A/D conversion input, remote control pulse detector	2k	128	24			SDIP30	-
KMP47C233AN	LED driver	3-bit A/D	1		36	1.0		CDTD49	
KMP47C433AN		conversion input	4k	256	30	1.9		SDIP42	
KMP47C242BN		LED driver	2k	128	23		2.7 to 6.0	SDIP30	KMP47P242VN
KMP47C440AN/AF	A/D converter,	rep anver			34			SDIP42	KMP47P440AN/AF
KMP47C441AN/AF	watchdog timer	VFT driver		256	34		4.5 to 6.0	QFP44	KMP47P441AN/AF
☆ KMP47C446ADF		LCD driver			24	1.9 (244)		QFP64	KMP47P446VDF
KMP47C451BN					23	16.7		SDIP30	KMP47P451VN
KMP47C452BN/BF	DTMF generator		4k		35		2.2 to 6.0	SDIP42	KMP47P452VN/VF
KMP47C453AN/AF						8.3		QFP44	KMP47P453VN/VF
KMP47C454AN			768	23			SDIP30	KMP47P454VN	
KMP47C407AN/AF					35	2.1	2.7 to 6.0	SDIP42 QFP44	KMP47P407VN/VF
KMP47C456ADF		LCD driver			34	8.3 (244)		QFP80	-

Note: ( ); the minimum instruction execution time when low-frequency clock is used.

\*\*These package types will be delivered in the short lead length package.

Type suffix

\*\*P! Plastic dual in-line package (DIP)

N: Plastic shrink dual in-line package (SDIP)

F: Plastic flat package (QFP)

\*\*G: Ceramic strandard flat package (QFC)

#### KLCS-470A Series

Product No	Function	ROM (bytes)	RAM (nibbles)	I/O port	Minimum instruction execution time (µs)	Power Supply Voltage (V)	Package	Built-in One-time PROM product	
KMP47C623F	LCD driver (24 to 20×4),	6k	384	20 40 00			QFP64	KMP47P823VF	
KMP47C823F	High-speed event counter	8k	512	32 to 28			QFF04	MWIP41P823VP	
KMP47C1220F	LCD driver (32×4),	12k	768	36			Octoo	KMP47P1620VF	
KMP47C1620F	High-speed timer/counter	16k	100	30	1.3 (244)	4.5 to 6.0	QFP80	MVIF4/F102UV F	
KMP47C637N		6k	384				SDIP42		
KMP47C837N	On-screen display circuit, D/A conversion (PWM) output, 4-bit A/D conversion input, Remote control pulse detector	8k	304	32				KMP47P1637VN	
KMP47C1237N		12k						KIVIP4/PI03/VIV	
KMP47C1637N		16k	512						
KMP47C1238AN		12k					SDIP54	123MD 47D1 6903 INT	
KMP47C1638AN		16k					SD1F34	KMP47P1638VN	
KMP47C853N/F		8k	1024		8.3 (244)	2.2 to 6.0		KMP47P853VN/VF	
KMP47C457N/F	DTMF generator	4k	768	35	0.1 (0.44)	074-00	SDIP42 QFP44	YZN 400 470001775 ZNY 67 ZYC	
KMP47C857N/F		8k	1024		2.1 (244)	2.7 to 6.0	Q. 1 -7-7	KMP47P857VN/VF	
KMP47C1260N/F	A/D converter (8bits×8ch),	12k		-10			SDIP64		
KMP47C1660N/F	Remote control pulse detector, LED driver	16k		56			QFP64	KMP47P1660VN/VF	
KMP47C1270AN	VFT driver (16×18 to 13× 16), D/A conversion (PWM)	12k	768	53	1.3 (244)	4.5 to 6.0	an India	773.400.4701.6703.733	
KMP47C1670AN	output, 4-bit A/D conversion input, Remote control pulse detector	16k		53			SDIP64	KMP47P1670VN	

note: ( ); the minimum instruction execution time when low-frequency clock is used.

Type N: Plastic shrink dual in-line package (SDIP)

E: Ceramic shrink dual in-line package (SDIC)

F: Plastic flat package (QFP)

G: Ceramic standard flat package (QFC)

## ■ KLCS-47E, 47, 470 series (Wide-temperature range/High-quality products)

Product No	Function	ROM (bytes)	RAM (nibbles)	I/O port	Minimum instruction execution time (µs)	Power Supply Voltage (V)	Operating temperature (°C)	Package	Built-in One-time PROM product (note 2)	
* KMP47C101WP	TOD 13	1k	64			2.2 to 5.5	-40 to 110	DIP16	Y20 400 457000013 70	
* KMP47C201WP	LED driver	2k	128			(note 3)	-40 to 110	SOP16	KMP47P201VP	
† KMP47E186M	E <sup>2</sup> PROM (16 bytes), SPI	1k	64	111	12	1.3	(note 4)	-40 to 85	SOP16	KMP47P186M (note 5)
KMP47E187M	E <sup>2</sup> PROM (16 bytes), SPI	1k	64		1.0	(note 4)	-40 to 85	SOP16	KMP47P187M	
KMP47C241IN/IM	A/D converter	01	100	21		0.7.4.60	-40 to 85	SDIP28	KMP47P241VN/VM	
KMP47C241WM	LED driver	2k	128			2.7 to 6.0	-40 to 110	SOP28	KMP47P24IVN	
† KMP47E885IF	E <sup>2</sup> PROM (64 bytes), PWM, UART A/D converter 16-bit timer/counter Input	8k	512	36		4.5 to 5.5	-40 to 85	QFP44	KMP47P885F	
† KMP47E885WF	Capture, Output compare						-40 to 110			

- \* : Under development
- †: USP4, 382, 279 owned by BULL CP8

Type suffix

- P: Plastic dual in-line package (DIP)
- M: Plastic small outline package (SOP)
- N: Plastic shrink dual in-line package (SDIP)
- F: Plastic flat package (QFP)

- note 1: If there is any further information you require when considering I/W version products, please contact our sales representative.
- note 2 : OTP built-in type is under consideration for high-temperature range/high-quality applications.
- note 3: During CR oscillation (2.7 to 5.5V when oscillator is connected).
- note 4: 2.7 to 5.5V when oscillator is connected, 2.0 to 3.4V during CR oscillation.
- note 5: KMP47P186M (CR oscillation), KMP47P187M (oscillator version)